Data-Intensive Systems in the Microsecond Era



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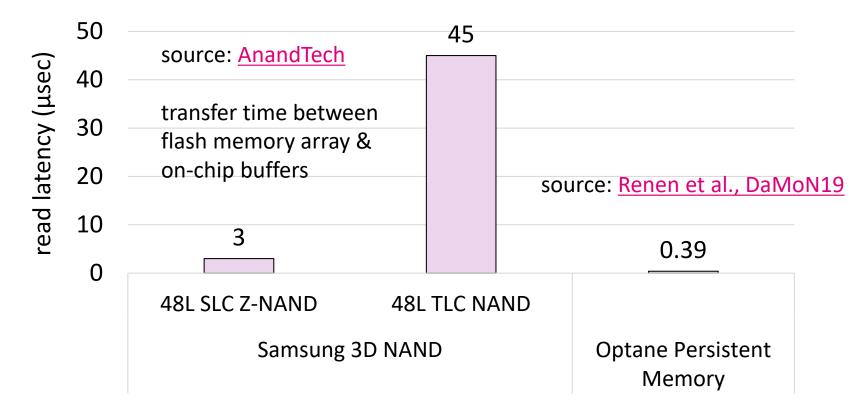
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state of the world we live in today

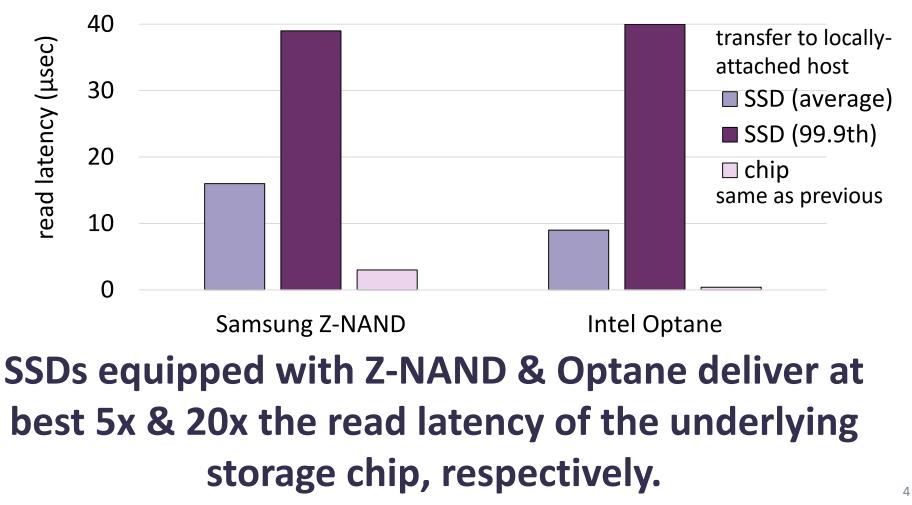
storage chips in the µsec era



Z-NAND storage chips are 8x slower than Optane memory, and 15x faster than existing NAND chips.

SSDs in the µsec era

4K random read using fio - source: AnandTech



interconnects in the µsec era

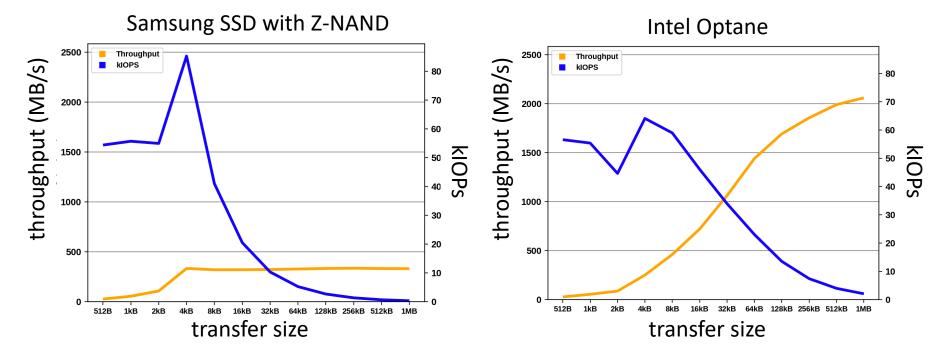
Not going to show numbers here.

Latency is of the same order as storage chip latency for fast interconnects.

Fast network-attached storage (RDMAbased) just adds to the latency of directattached storage (PCIe).

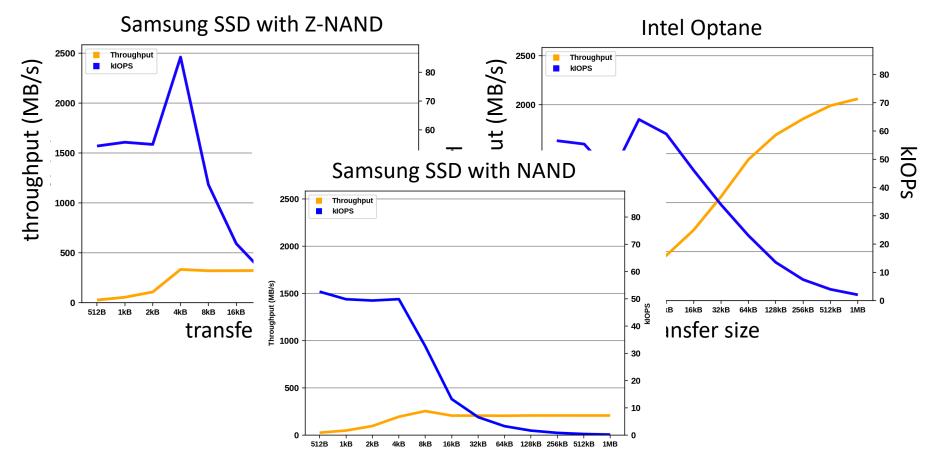
ftls in the µsec era ...

random writes- source: AnandTech



ftls in the µsec era ...

random writes- source: AnandTech

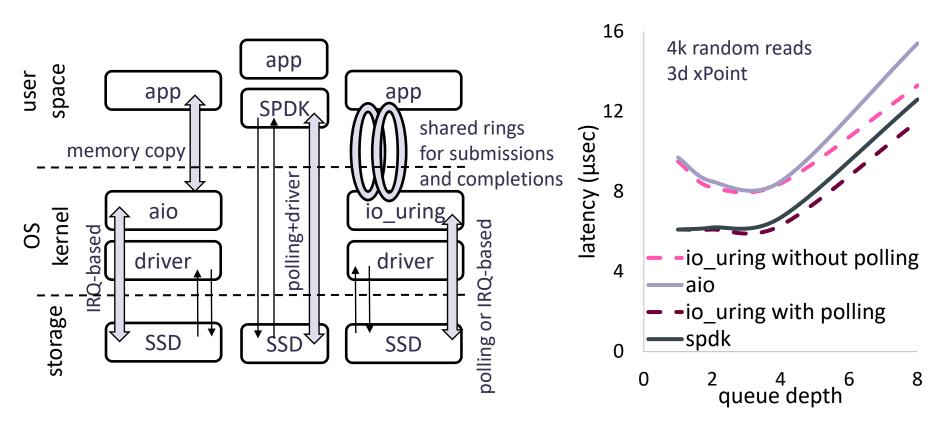


... have even more drastic impact on throughput!

linux IOs in the µsec era

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sources: <u>Faster IO through io_uring &</u> <u>Efficient I/O with io_uring & J.Axboe</u>



separation of control & data plane in linux now, POSIX out zero copy & minimized synchronization overhead

the benefits of fast storage wasted by

- data movement overheads
- (from device to host & across network)
- black-box generic flash-translation layers
- multitude of software layers

how do we prevent these?

back when I was 10 ...

Put Everything in Future (Disk) Controllers (it's not "if", it's "when?")

Jim Gray

http://www.research.Microsoft.com/~Gray

Acknowledgements: Dave Patterson explained this to me a year ago Kim Keeton Erik Riedel Catharine Van Ingen



Basic Argument for x-Disks

- Future disk controller is a super-computer.
 - »1 bips processor
 - ≫128 MB dram
 - >>100 GB disk plus one arm
- Connects to SAN via high-level protocols
 » RPC, HTTP, DCOM, Kerberos, Directory Services,....
 » Commands are RPCs
 » management, security,....
 » Services file/web/db/... requests
 » Managed by general-purpose OS with good dev environment

Move apps to disk to save data movement >> need programming environment in controller

Jim Gray, NASD Talk, 6/8/98 http://jimgray.azurewebsites.net/jimgraytalks.htm

today ...



8-core ARMv8 processor

32GB DRAM

2TB+ of NVM via M.2 slots

4x 10Gb Ethernet

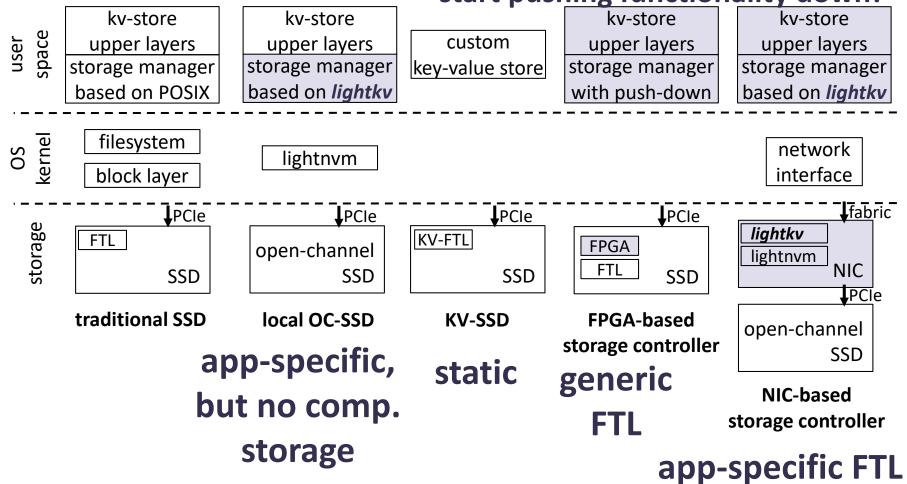
Dragon Fire Card (DFC) https://github.com/DFC-OpenSource/

Future disk controller is a super-computer.
 >> 1 bips processor
 >> 128 MB dram
 >> 100 GB disk plus one arm

SSD landscape

kv-store needs to change when you

start pushing functionality down!



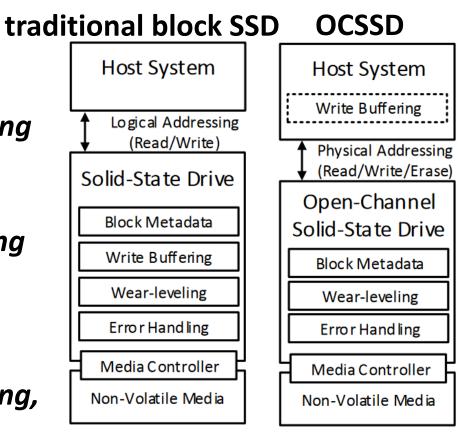
open-channel SSDs

physical address space exposed

 host can make decisions about data placement & I/O scheduling

SSD management split between

- back-end (embedded on SSD)
 block metadata & wear levelling (for warrantee)
- front-end (host-based) FTL mapping of logical to physical address spaces, overprovisioning, & garbage collection separates (application)



separates (*application-customizable*) front-end from (*media-specific*) back-end

potential impact

WHAT?

I/O isolation

- host management of device internal resources for contention avoidance
- control latency predictability
 - beyond NVMe IO determinism

resource utilization

- controlled data placement to reduce write amplification
 - beyond NVMe streams

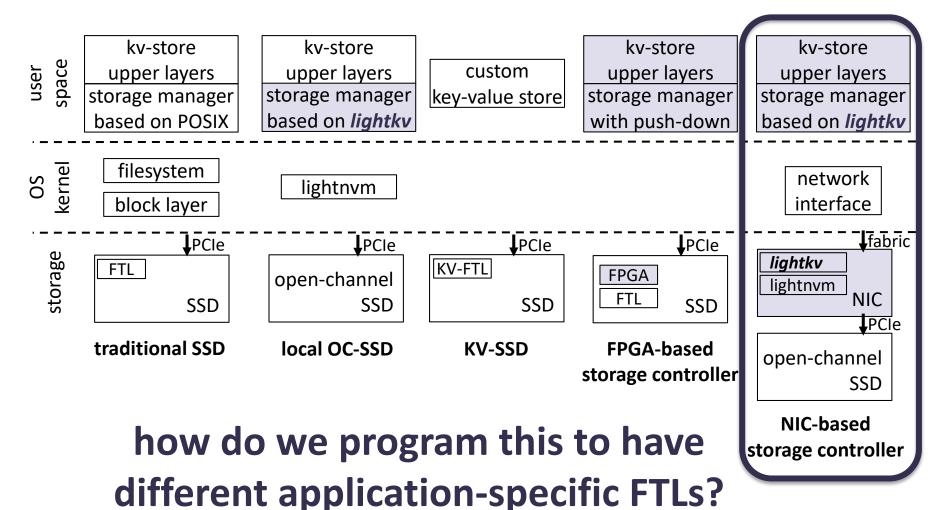
streamline data path

• application-specific FTL

HOW? computational storage

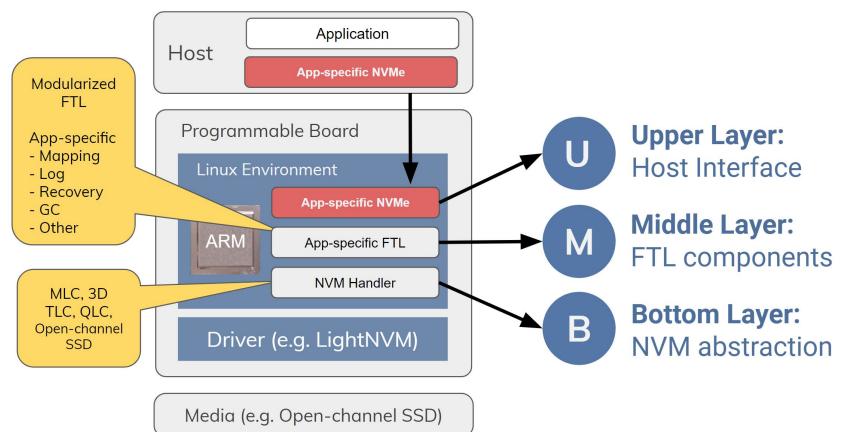
- offload CPU
- shield host application from complexity of managing the physical space (e.g., flash characteristics)
- co-design of applicationspecific FTL and OCSSD

SSD landscape



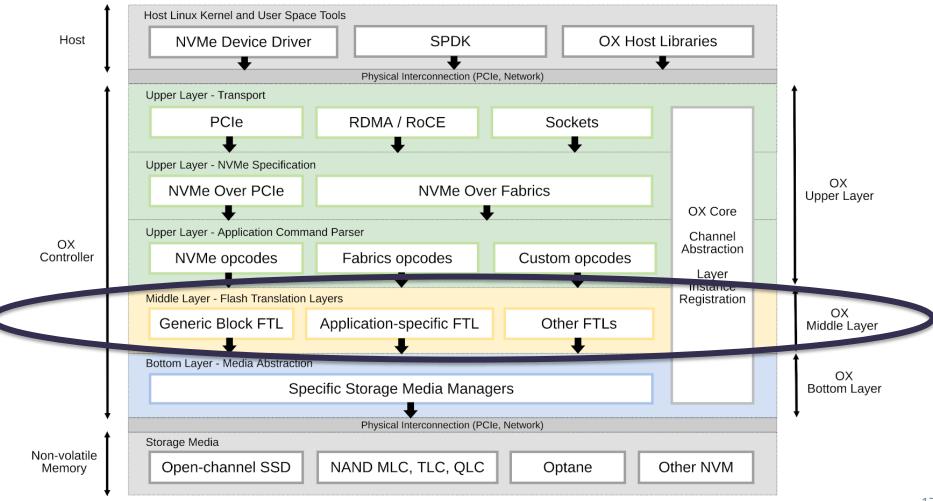
programming interface



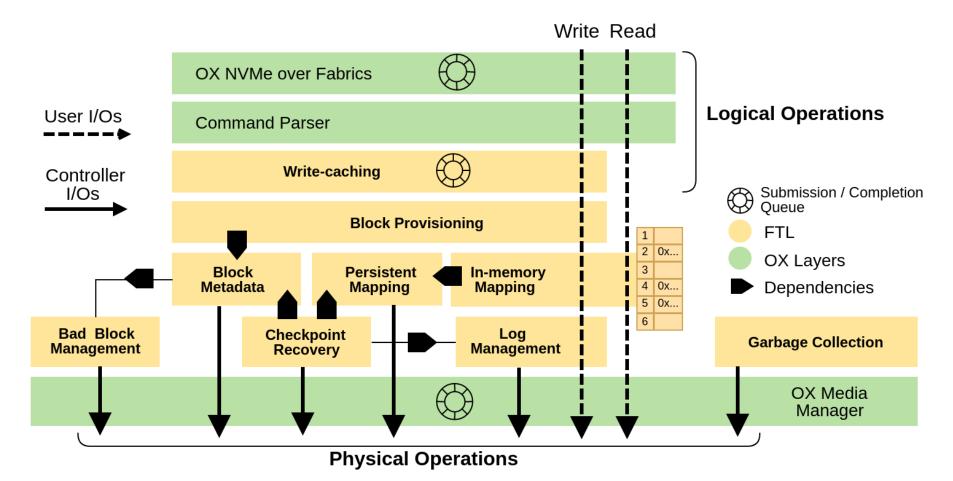


OX to program storage controllers

https://github.com/DFC-OpenSource/ox-ctrl



deconstructing FTL with OX



conclusion

- need to be careful about data movement
 - computational storage would help
- application-specific FTLs would naturally allow computational storage on SSDs
 - use cases:
 - LSM (ongoing work in our lab using RocksDB)
 - BwTree (what Dave Lomet talked about during gongshow)
- open issues
 - ZNS (zoned namespaces)
 - what to push down?