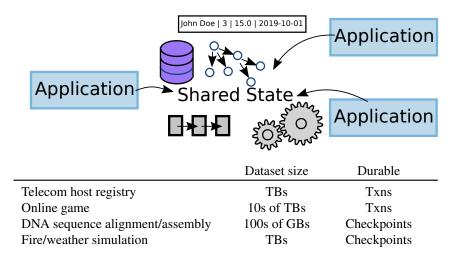
# Fast, fault-tolerant transactions as an OS extension

#### Benjamin A. Braun, David Cheriton

Stanford University

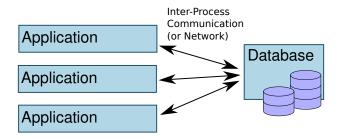
HPTS 2019

#### Motivation: Complex, durable state



Large, ad-hoc shared state, with durability, parallelism

#### Database Management Systems



Queries can feel limiting & inconvenient to developer

Socket communication round-trip time on data access path (  $>2 \,\mu s$  local,  $>7 \,\mu s$  in-datacenter)

• DBMS aren't always the right solution

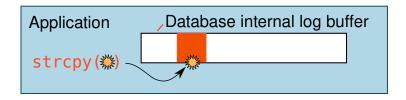
#### Database libraries arose to serve in-memory data faster



Transactions in terms of simple, efficient function calls to library

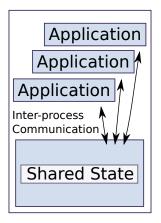
• Libraries offer durability, memory-speed access

#### Application bugs with database libraries

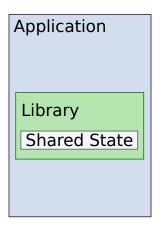


Buggy application "scribbles" over database datastructures  $\implies$  data loss, possibly corruption

• The root problem is a lack or isolation

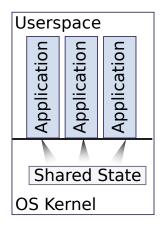


• DBMS applies changes. Process isolation. Communication cost.



• Database library applies changes. No isolation.

#### OS isolation: Transactional Virtual Memory



• OS kernel applies changes. Shared memory with isolation. **Transactional virtual memory**.

#### TVM vs. database libraries, DBMS

- Crash failures: Protected.
- Application bugs: Some protection.
  - Write scribbles don't hit durable data unless committed
  - Shared state isolated from application bugs
- Data structure: Unstructured.
  - Same API as software transactional memory
  - No support for queries
- Toolkit: debugger, performance traces, load balancing, resource limiting, access control, work as-is from OS.
  - Mostly same as libraries
  - DBMS IPC breaks control flow

TVM combines useful properties of both DBMS, libraries

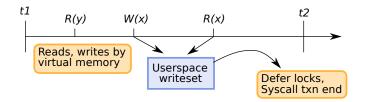
OS locks slower than memory locks (>1.4  $\mu$ s vs >200 ns)

Puts system call (>400 ns) on the commit path

Puts page fault (>1.2  $\mu$ s) on the data access path

• Seems dead in the water.

#### Transaction processing tricks



- Defer locking until commit, snapshot isolation
- Avoid repeated faults, keep pages across txns
- Buffer writes in userspace, read-your-writes cache

Getting the most out of each OS entrance

Given SI-safe dictionary, arrays, queues, can do most tasks

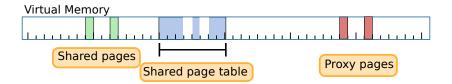
Conflict-on-free: Allocator writes zeros on free

• Fixes anomalies deriving from use-after-free

Automatic correction of snapshot isolation anomalies [Litz, et.al., 2015]

Have tools to avoid snapshot isolation anomalies

Litz, Heiner, Ricardo J. Dias, and David R. Cheriton. "Efficient correction of anomalies in snapshot isolation transactions." ACM Transactions on Architecture and Code Optimization (TACO) 11.4 (2015): 65.

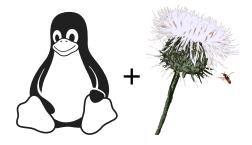


- Shared pages, page tables for write-cold data
- Proxy pages for write-hot data

Can use heuristics to balance frequent faults with upkeep

- 1. We show TVM achieves durable transaction performance >95% that of software transactional memory
- 2. We demonstrate porting parallel applications to TVM, adding fault-tolerance without massive application changes
- 3. We prove that TVM can be implemented as a simple extension to a conventional OS

#### Implementation of TVM for Linux: Thistle





<u>Th</u>read <u>Is</u>olating <u>T</u>ransactions as Kerne<u>I</u> <u>E</u>xtension. (Thistle)

#### Evaluation

Implementation of TVM for Linux: Thistle

Comparison vs DBMS: MySQL, KeyDB

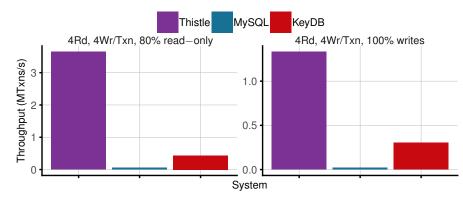
Comparison vs software transactional memory (STM) + write-ahead logging for durability. Systems tested:

- A snapshot isolation variant of TinySTM.
- SI-TM inspired Multi-version concurrency control (MVCC)

Experiment controls:

- Same implementation of write-ahead logging
  - 5µs simulated log persistence latency
- Same implementation of transactional malloc / free
- Lock / timestamp table 100K entries, 64-byte granularity
- Snapshot isolation transactions

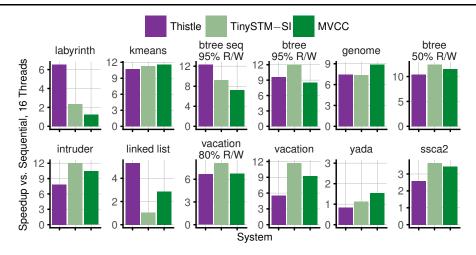
# TVM performance vs DBMS



- Stark contrast between inter-process communication (IPC – DBMS) and in-memory access (TVM).
- Note: KeyDB does not scale past 4 threads

IPC-based isolation a significant overhead for DBMS.

## TVM performance vs durable STM



Virtual memory manipulations can take up to 50% runtime For 8 out of 12 experiments, relative performance is >80%. Relax requirement for view to match snapshot timestamp

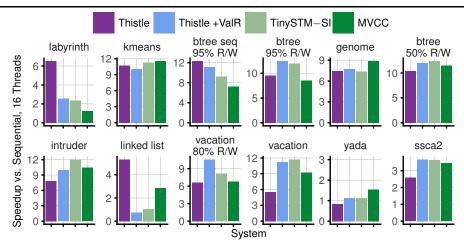
• Timestamp validated cache line reads in userspace TM\_READ(word\* pointer): word toRet = \*pointer if (TIMESTAMP\_TABLE[hash(address)] > snapshot\_ts) then abort

What do we lose?

- Unmodified code can't read safely
- Single version  $\implies$  read-after-write aborts
- Sequential reads slower due to validation

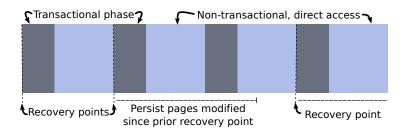
Read validation avoids costs of page table manipulation, loses some of the benefits of snapshot isolation

## TVM + validating reads vs durable STM



>95% the speed of state-of-the-art STMs on 8 benchmarks

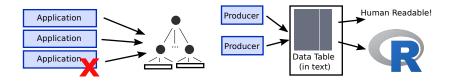
Eliminates most overheads, OS entry/exit up to 10% runtime



- Particle simulation, 4GB of particles, sharded by position
- · Move particles (non-txnal) then reshard each round (txnal)
- Recovery point every 1s adds <3% overall overhead.
  - Reference: Dumping 4GB striped across 2 SSDs takes  ${\sim}1s$

TVM gives both checkpoint durability and txnal durability

#### More uses



- Memory-speed KV-store that tolerates bugs, kill signals
- R analysis on a changing data table file (700MB, text)
  - Time to load data table <3% longer than from memory file.

Applications with ad-hoc data format, concurrency control, but don't need structured queries.

- Loadable kernel module for Linux v4.2.1+ and a userspace library libThistle
- New filesystem type: thistle

# Lines of code	
	Thistle kernel module
966	Thistle userspace library

0 Lines of code changed in Linux.

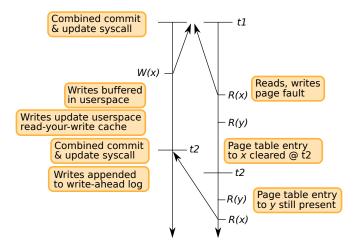
For comparison: Linux mm subsystem is  $\sim$ 80k lines.

• TVM can be implemented without changes to Linux.

# The OS is not the enemy.

**I'm looking for an job in industry.** bimnbraun at gmail dot com (Backup slides.)

#### Transaction processing in Thistle



Snapshot isolation transactions Userspace creates writeset, read-your-writes cache

#### Characterizing TVM performance

TVM handles two cases especially well, compared to STMs:

- Heavily read datastructures with concurrent modifiers
  - Reads to concurrently written state lead to private pages
  - Reads to private pages never abort
  - Private pages never have false sharing
  - ~1.8x, 2.8x speedups on labyrinth, linked-list
- Sequential access
  - Once a page is mapped into a threads' view, reads are free.
  - 1.3x speedup on sequential btree 95% reads
- TVM tends to lose for high degrees of write contention.
  - No system handles these well
  - High contention is an anti-pattern

#### Application changes to use TVM

- Each thread must be its own process
  - i.e. pthread\_spawn() -> fork()
- Application must place shared data in TVM-managed files
  - Modified allocator provided for this purpose
  - Global variables might need to be refactored
- Add transaction { } blocks to denote transactions
  - Code transformations achievable with modern compilers

Code changes to use TVM are minor.

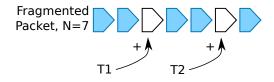
#### Porting STAMP suite to OS-level TM

STAMP suite details: 7 applications,  ${\sim}18K$  lines of code

# Lines	Reason for change
85	Add multi-process support (fork, thread barriers, etc.)
240	Place shared data into transactionally managed segment
14	Snapshot isolation transaction model (intruder)
339	Total lines of code changed

Overall: <2% of lines changed to port STAMP to Thistle.

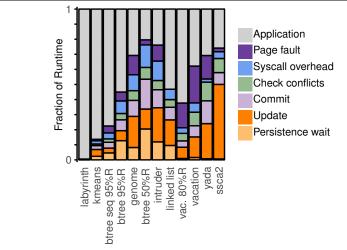
#### Snapshot-isolation anomaly in intruder



```
transaction {
    fragment = QUEUE_POLL(fragmentChannel);
    packet = FIND_PACKET(fragment.packetID);
    exists = LIST_INSERT(packet, fragment);
    if (exists) { goto error; }
    long list_size_after = LIST_GETSIZE(packet);
    <u>if (list_size_after == packet.N) {
        QUEUE_PUSH(completePackets, packet);
    }
}</u>
```

• Adding a length field to the packet list resolves the anomaly in just 14 lines of code.

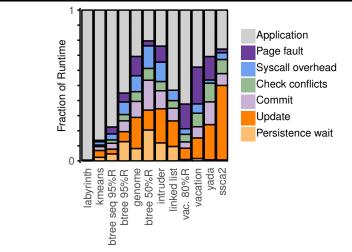
#### Performance breakdown



- Page fault at most 26%
- Syscall overheads at most 15% (mostly in syscall entry)

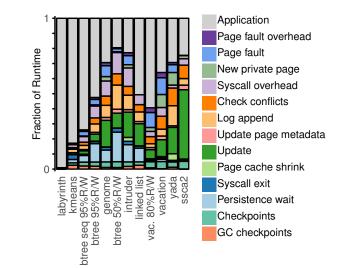
Largest overheads: Page faults, syscalls, and updating page map.

#### Performance breakdown



- · For yada, update dominated by software page table walks
- For ssca2, update dominated by applying writes to private pages Largest overheads: Page faults, syscalls, and updating page map.

#### Performance breakdown (detailed)



#### Breaking down pgfault overheads

Overhead	Command	s', Event count (appr Shared Object	Symbol
	btree_benchmark	btree_benchmark	
	btree_benchmark		[k] sivfs_fault
	btree_benchmark	[kernel.kallsyms]	[k] down_read
	btree_benchmark	[kernel.kallsyms]	[k] page_fault
	btree_benchmark	[kernel.kallsyms]	[k]handle_mm_fault
	btree_benchmark	[kernel.kallsyms]	<pre>[k] swapgs_restore_regs_and_return_to_usermode</pre>
	btree_benchmark	[kernel.kallsyms]	[k] error_entry
	btree_benchmark		[k] sivfs_snapshot_view_prepare_access
	btree_benchmark	[kernel.kallsyms]	[k] handle_mm_fault
	btree_benchmark		<pre>[k] sivfs_checkpoint_traverse_partial</pre>
	btree_benchmark		[k] _sivfs_snapshot_view_map_cp_children
	btree_benchmark	[kernel.kallsyms]	[k]radix_tree_lookup
		[kernel.kallsyms]	[k]do_page_fault
	btree_benchmark	[kernel.kallsyms]	[k] handle_pte_fault
		[kernel.kallsyms]	[k] sync_regs
	btree_benchmark		[k] up_read
		[sivfs]	<pre>[k] sivfs_persistence_threadfn</pre>
	btree_benchmark		[k] sivfs_task_to_state
		[kernel.kallsyms]	[k] vmacache_find
			[k] _raw_spin_lock
			[k] down_read_trylock
+ 0.45%			[k] _sivfs_snapshot_view_invalidate_range
+ 0.44%		[kernel.kallsyms]	<pre>[k] pmd_devmap_trans_unstable</pre>
			<pre>[k] find_get_entries</pre>
		[kernel.kallsyms]	[k] mem_cgroup_from_task
		[kernel.kallsyms]	<pre>[k] native_queued_spin_lock_slowpath</pre>
			[k] find_vma
	btree_benchmark	[kernel.kallsyms]	<pre>[k] prepare_exit_to_usermode</pre>

- Poor scaling in down\_read on mmap\_sem
- Significant software overheads in page\_fault, \_handle\_mm\_fault.

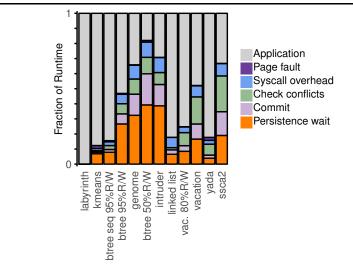
#### **TLB** Invalidation costs

Our test machine (Intel x86) provides a single-page TLB flush instruction as well as a full TLB flush instruction.

% Speedup:	Entire TLB flush
btree seq 95%R/W btree 95%R/W btree 50%R/W genome intruder kmeans labyrinth linked list ssca2 vacation vaca. 80%R/W	1 1 5 1 1 0 12 0 1 2 1
yada	8

- The single-page TLB flush instruction is sufficiently expensive that calling it multiple times is prohibitive.
- Flushing the entire TLB instead after update (when at least one page is changed) provided up to a ~10% speedup.

#### Performance breakdown: Validating reads



 Validating reads eliminates page table manipulation costs, at the price of requiring code modification to read